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PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Kin Pui KWAN et al. Serial No.: 09/802,678 Filing Date: March 9, 2001 For: LEADLESS PLASTIC CHIP CARRIER WITH ETCH BACK PAD SINGULATION	Art Unit: 2826 Examiner: M. Tran
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DECLARATION UNDER 37 C.F.R. § 1.132

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

1. I, Neil MCLELLAN, hereby declare that I am an inventor in the present application.
2. I, Neil MCLELLAN, hereby declare that I invented all of the subject matter disclosed in U.S. Patent No. 6,545,347 and relied on in a prior art rejection of the claims in the presently pending U.S. Patent Application No. 09/802,678.
3. I, Neil MCLELLAN, hereby declare that I invented the common subject matter disclosed in U.S. Patent No. 6,545,347 and the presently pending U.S. Patent Application Serial No. 09/802,678.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.


Neil MCLELLAN

2-2-05
Date

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09/799,968

ENHANCED LEADLESS CHIP CARRIER

Application Data

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Class / Subclass:	257/690	Patent Number:	6,545,347
First Named Inventor:	Neil Mclellan , Hong Kong, (CN)	Issue Date of Patent:	04-08-2003
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(54) **ENHANCED LEADLESS CHIP CARRIER**

(52) U.S. CL. 257/690

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(57) **ABSTRACT**

A plastic integrated circuit package includes a lead frame having numerous leads, a die attach pad and a ground ring. In one embodiment, the plastic integrated circuit package is provided as a plastic leadless chip carrier. Slots provided between the die attach pad and the ground ring provides support and prevent delamination from the plastic molding compound and enhanced moisture-resistance, thus resulting in a highly reliable integrated circuit package, even in the face of high temperature cycles, such as solder reflows.

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